



PATENT APPLICATION
Do. No. 1482-138

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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TECHNOLOGY CENTER 2800
FEB - 4 2002

In re application of: Barrie Gilbert

Serial No. 09/694,731

Examiner:

Quan Tra

Filed: October 23, 2000

Group Art Unit: 2816

For: LOW SUPPLY CURRENT RMS-TO-DC CONVERTER

Date: October 18, 2001

I HERESY CERTIFY THAT THIS
CORRESPONDENCE IS BEING DEPOSITED
WITH THE UNITED STATES POSTAL
SERVICE AS FIRST CLASS MAIL IN AN
ENVELOPE ADDRESSED TO:

☐ COMMISSIONER OF PATENTS AND
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☐ ASSISTANT COMMISSIONER FOR
TRADEMARKS 2900 CRYSTAL DRIVE
WASHINGTON, VA 22202-3513

Assistant Commissioner for Patents
Washington, DC 20231

RESPONSE TO OFFICE ACTION

Responsive to the Office Action, dated July 18, 2001, please amend the application as follows.

IN THE CLAIMS

Please amend the claims by rewriting as follows.

1. (Amended) A method for operating a transistor cell comprising an input terminal for receiving an input signal, an output terminal for transmitting an output signal, a grounded base transistor coupled between the input and output terminals, and a current mirror coupled between the input and output terminals, the method comprising:

biasing the transistor cell to establish a bias current in the grounded base transistor and the current mirror when the input signal is zero; and

limiting the input signal to a range in which the output function of the transistor cell approximates a square-law.

2. (Cancelled)

Ok To enter.
9/8/01